















Diagram illustrating the internal structure and data flow of a control unit, showing four sequential loops (Loop 1) and their associated memory and control signals.

Legend:

- A:** Address
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Loop 1 (Left):

- Control Signals:** 3X1¹⁰⁵, 3X1¹⁰⁶, SE-9.1M Stop, SE-9.1M Start, SE-10.1M Stop, SE-10.1M Start.
- Memory Structure:** M1 (1-4), M2 (1-3), M3 (4-6), M4 (4-6), M5 (4-6). Address range: ADR 33 to ADR 40.
- Data Flow:** KR2.1¹¹, KR2.1¹⁴, KR2.2¹¹, KR2.2¹⁴, KR3.1¹¹, KR3.1¹⁴, KR3.2¹¹, KR3.2¹⁴.

Loop 1 (Middle-Left):

- Control Signals:** SE-11.1M Stop, SE-11.1M Start, SE-12.1M Stop, SE-12.1M Start.
- Memory Structure:** M1 (1-4), M2 (1-3), M3 (4-6), M4 (4-6), M5 (4-6). Address range: ADR 41 to ADR 48.
- Data Flow:** KR4.1¹¹, KR4.1¹⁴, KR4.2¹¹, KR4.2¹⁴, KR5.1¹¹, KR5.1¹⁴, KR5.2¹¹, KR5.2¹⁴.

Loop 1 (Middle-Right):

- Control Signals:** SE-13.1M Stop, SE-13.1M Start, SE-14.1M Stop, SE-14.1M Start.
- Memory Structure:** M1 (1-4), M2 (1-3), M3 (4-6), M4 (4-6), M5 (4-6). Address range: ADR 49 to ADR 56.
- Data Flow:** KR6.1¹¹, KR6.1¹⁴, KR6.2¹¹, KR6.2¹⁴, KR7.1¹¹, KR7.1¹⁴, KR7.2¹¹, KR7.2¹⁴.

Loop 1 (Right):

- Control Signals:** SEV1 START, SEV2 START.
- Memory Structure:** M1 (1-4), M4 (1-3), M5 (4-6). Address range: ADR 57 to ADR 64.
- Data Flow:** KR8.1¹¹, KR8.1¹⁴, KR8.2¹¹, KR8.2¹⁴, KR9.1¹¹, KR9.1¹⁴, KR9.2¹¹, KR9.2¹⁴, KR10.1¹¹, KR10.1¹⁴, KR10.2¹¹, KR10.2¹⁴.

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